

Docket No.: 43889-937



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Sadami Takeoka et al.	:	Confirmation Number: 3437
Serial No.: 09/843,687	:	Group Art Unit: 2825
Filed: April 30, 2001	:	Examiner: DO, THUAN V
For: METHOD OF DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT	:	

**AMENDMENT UNDER 37 C.F.R. §1.111**

Mail Stop Amendment  
Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 10, 2005, having a three-month shortened statutory period for response set to expire September 10, 2005, please amend the application as follows: